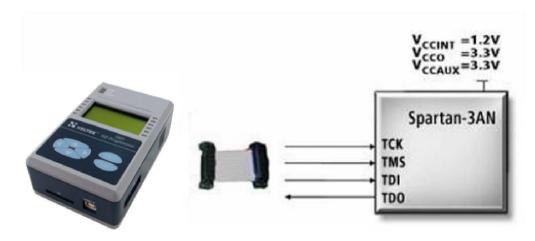
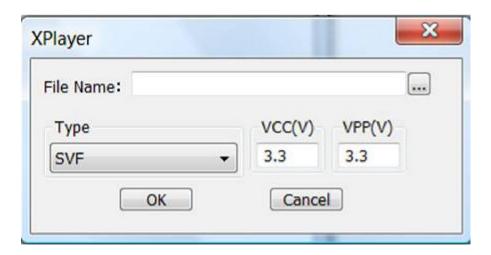
## SuperPro IS01 Supports Xilinx Spartan 3AN FPGA

Programming a Xilinx Spartan 3AN FPGA device requires running an SVF player, and generating a programmable data file with \*.svf extension.

<u>XPlayer</u> is a special programming tool which resides within <u>SuperPro IS01</u> and can be used for programming Xilinx FPGA devices. As long as the customer provides corresponding SVF file (similar files can be generated using design tools such as <u>iMPACT</u>), the device can be programmed.



dialogue box displayed below, appears in SPIS01 software when "XPlayer" is selected on the "Device" menu bar.



Device related information is already included within SVF file; therefore there is no need to select any device for programming.

When "device information" in tool bar or in the menu of <u>SuperPro IS01</u> software is opened, the cable connection (JTAG signals) can be seen as follows:

```
🎇 Dev. Info
                                                                            ISP connector of ISB1 programmer:(front view at connector)
                8 10 12 14 16 18 20
Description of ISP connector pins:
1 - GND (USS)
2 - GND (USS)
3 - Don't connect!
4 - Don't connect!
5 - TDO
6 - TDI
7 - TCK
8 - TRST
9 - Don't connect!
10 - TMS
11 - Don't connect!
12 - Don't connect!
13 - Don't connect!
14 - Don't connect!
15 - Don't connect!
16 - Don't connect!
17 - UDD
18 - UDD
19 - Don't connect!
20 - Don't connect!
```

Internal supply voltage (VCCint) of <u>Spartan family of Xilinx FPGA</u> is 1.2V, and VCCaux, VCCo are 3.3V.

<u>SuperPro IS01</u> can supply 3.3V, however 1.2V is not supported. Users need external power supply to support internal supply voltage (VCCint).

Therefore, connect VCC or VPP (3.3V) of <u>SuperPro IS01</u> to VCCaux, VCCo, Vref of Xilinx FPGA; and supply VCCint (core work VCC=1.2V) via an external source. Also connect GND (ground) of external power supply (1.2V) to GND of <u>SuperPro IS01</u>.

## Supported devices are:

Xilinx CPLD: XC9500 / XC9500XL / XC9500XV / CoolRunner / CoolRunner-II

Xilinx PROM: XC18V00 / XCF00S / XCF00P

Xilinx FPGA: Virtex Families, Spartan Families, and JTAG-configurable XC4000 Families

If you need to search other programmable Xilinx devices, please check the <u>device search tool</u>. And for questions you may have, please feel free to contact us at <u>techsupport@xeltek.com</u>